ABSTRACT OF THE DISCLOSURE

A device for conditioning children to perform special tasks that utilizes a generated signal controlled by a central processor unit ("CPU") which acts to, among other things, establish normal time intervals so that the signal is activated at the end of that interval. An accomplishment switch will signal and reset the timer to the normal time interval when activated. A clock is associated with the CPU to show the time remaining before the next signal activation, and visual means is provided to display timer intervals and real time. The generated signal can be audible, visual, or vibratory, or combinations thereof, and visual means can be utilized with the device to selectively display graphics during the time interval and/or upon its termination. A reward feature is incorporated in or with the device to encourage a willingness to participate on the part of the child or children being trained.